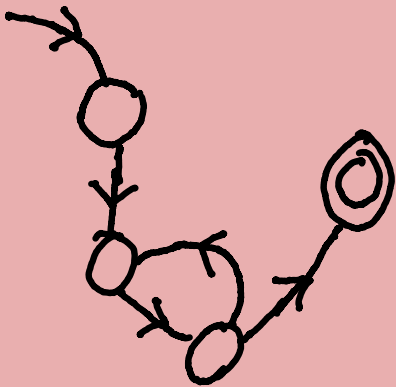


```
int main() {  
    for(int i=0; i<N; i++)  
        return i;  
}
```

.c

⇓ HLS

```
module top();
```



```
endmodule
```

.v

⇒
Logic
Synthesis

